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Docket No.: L&L-I0062 I hereby certify that this correspond eposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, P.O Box 1450, Alexandria, VA 22313-1450. 3/25/09 Date:

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applic. No.

: 10/624.955

Confirmation No: 6278

Applicant

Bernd Schmandt

Filed

July 22, 2003

Title

Method for Operating a PLL Frequency Synthesis Circuit

Docket No.

L&L-10062

Customer No.

: 24131

REQUEST FOR FILING RECEIPT CORRECTION

Hon. Commissioner of Patents and Trademarks,

Sir:

Undersigned counsel has received the Filing Receipt for the above-identified application.

However, the priority information was listed incorrectly and should be as follows:

THIS APPLN IS A CON OF

PCT/DE02/00171

January 21, 2002

The foreign application information was listed incorrectly and should be as follows:

GERMANY

101 02 725.7

January 22, 2001

It is respectfully requested that the Patent Office Records be changed and that a new Filing Receipt be issued, so that the printed patent will show the correct priority and foreign application information.

Respectfully submitted

LÓREN DONALD PEARSON

REG NO., 42,987

Enclosed:

Copy of the Filing Receipt

Copy of the first page of the Declaration Copy of the first page of the Specification

/av

Lerner and Greenberg, P.A.

Post Office Box 2480

Hollywood, FL 33022-2480 (954) 925-1100

Tel: Fax:

(954) 925-1101



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L&L-10062 :/

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LERNER AND GREENBERG, P.

Post Office Box 2480 Hollywood, FL 33022-2480



CONFIRMATION NO. 6278

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OC000000011381095*

Date Mailed: 12/01/2003

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

Bernd Schmandt, Wuppertal, GERMANY;

Domestic Priority data as claimed by applicant

Foreign Applications

GERMANY 101 02 725.7 01/22/2001 4 GERMANY PCT/DE02/00171 01/21/2002

is a CON of

If Required, Foreign Filing License Granted: 11/28/2003

Projected Publication Date: To Be Determined - pending completion of Missing Parts

Non-Publication Request: No

Early Publication Request: No

Title

Method for operating a PLL frequency synthesis circuit

Preliminary Class

LICENSE FOR FOREIGN FILING UNDER Title 35, United States Code, Section 184 Title 37, Code of Federal Regulations, 5.11 & 5.15

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L&L-I0062

METHOD FOR OPERATING A PLL FREQUENCY SYNTHESIS CIRCUIT

5 Cross-Reference to Related Application:

This application is a continuation of copending International Application No. PCT/DE02/00171, filed January 21, 2002, which designated the United States and was not published in English.

10 Background of the Invention:

Field of the Invention:

The invention relates to a method for operating a PLL frequency synthesis circuit for a TDMA/FDMA data transmission device.

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Time division multiple access (TDMA) methods and frequency division multiple access (FDMA) methods are multiple access methods that are used in a large number of data transmission systems (cordless telephony, data interchange between computers and peripherals, etc.). TDMA is based on the subdivision of the time into time slots, which are then used for interchanging data in portions between terminals (for example base stations and mobile stations). FDMA is analogous to TDMA in the frequency domain, that is to say it relates to the subdivision of the overall transmission bandwidth into



COMBINED DECLARATION AND POWER OF ATTORNEY IN ORIGINAL APPLICATION

As a below named inventor, I hereby declare that: my residence, post office address and citizenship are as stated below next to my name; that I verily believe that I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled:

METHOD FOR OPERATING A PLL FREQUENCY SYNTHESIS CIRCUIT

described and claimed in the specification bearing that title, that I understand the content of the specification, that I do not know and do not believe the same was ever known or used in the United States of America before my or our invention thereof, or patented or described in any printed publication in any country before my or our invention thereof or more than one year prior to this application, that the same was not in public use or on sale in the United States of America more than one year prior to this application, that the invention has not been patented or made the subject of an inventor's certificate issued before the date of this application in any country foreign to the United States of America on an application filed by me or my legal representatives or assigns more than twelve months prior to this application, that I acknowledge the duty to disclose to the Office all information known to me to be material to patentability as defined in 37 CFR § 1.56, and that no application for patent or inventor's certificate of this invention has been filed earlier than the following in any country foreign to the United States prior to this application by me or my legal representatives or assigns:

German Application 101 02 725.7, filed January 22, 2001, the International Priority of which is claimed under 35 U.S.C. § 119; and International Application PCT/DE02/00171, filed January 21, 2002, the Priority of which is claimed under 35 U.S.C. § 120.

I hereby appoint the following attorney(s) and/or agent(s) to prosecute this application and to transact all business in the Patent and Trademark Office connected therewith:

> LAURENCE A. GREENBERG (Reg. No. 29,308) WERNER H. STEMER (Reg. No. 34,956) RALPH E. LOCHER (Reg. No. 41,947) GREGORY L. MAYBACK (Reg. No. 40,719)

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